

**CSIR-CENTRAL ELECTRONICS ENGINEERING RESEARCH INSTITUTE,**

**PILANI (RAJ.)**

**Advt. No. PF-06/2025**

**Dated: 25.07.2025**

**Online Interview/In Person Interview**

CSIR-Central Electronics Engineering Research Institute (CSIR-CEERI) is a premier research Institute in the field of Electronics, set up under the Council of Scientific & Industrial Research (CSIR). The Institute is looking for qualified candidates for the following project staff positions purely temporary engagements for various ongoing projects in the Institute.

**Candidates are requested to read “[Instruction To Apply](#)” before apply.**  
**Online applications are invited from candidates for the Engagement of Project Staff.**

**Details of Vacancies & Eligibility**

<b>Post code</b>	<b>Project No. &amp; Title</b>	<b>Area/Group</b>	<b>Name &amp; Number of Positions</b>	<b>Essential Qualification as on the last date of receipt of online applications i.e. 13.07.2025</b>	<b>Age</b>	<b>Consolidated Emoluments Per month (fixed)</b>
3293	MMP015201(W.P 2.8) & Design and development of antenna for THZ RADAR	High Frequency Devices and Systems	PAT-I & 01	First class B.E/ B.Tech./ M.Sc (Electronics/ECE/EEE/Electronics & instrumentation./ Electronics & Telecommunication/ Physics) or equivalent or M.E/ M.Tech. ( Electronics/ ECE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave) or equivalent	35 Years	Rs. 25000/-
3294	MMP015201(W.P 2.7) & Design and development of THz vacuum electronic amplifier.	Vacuum Electron Devices Group	PAT-I & 01	First class B.E/ B.Tech./ M.Sc (Electronics/ECE/EEE/Electronics & instrumentation./ Electronics & Telecommunication/ Physics) or equivalent or M.E/ M.Tech. ( Electronics/ ECE/ Electronics & Instrumentation/ Electrical Engg./ Electronics & Telecommunication/ Microwave) or equivalent	35 Years	Rs. 25000/-
3295	IHP202407 & Portable Embedded System for Methylene Blue Dye Test in Milk Quality Assessment	Cyber Physical System	PAT-I & 01	B.E/B.Tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics & Communication Engineering / Electronics and Instrumentation or equivalent	35 Years	Rs. 25000/- or Rs. 31000/- with GATE/NET
3296	CLP3465 & Autonomous drones for aerial and close up survey	Advanced Information Technology Group	PAT-II/I & 01	B.Tech in CS/IT/ECE/Robotics/ EEE/ Mech. / Mechatronics/ Instrumentation or equivalent and 2 years of experience in industrial/ academic or R&D dept.	35 Years	Rs. 28000/- or Rs. 35000/- with GATE/NET

	of agriculture and farm land			or M.Tech in CS/IT/ECE/Robotics/EEE/ Mechanical / Mechatronics/ Instrumentation or equivalent Desirable: Proficiency in Python, ROS, software development or drone navigation & control.		
3297	GAP-3250 & Development of wide- bandgap semiconductors based highly efficient power electronics systems for Electric Vehicle WP-1.1: Design and Development of high power D-E mode GaN HEMT	Semiconductor Sensors and Microsystems Group (SSMG)	JRF & 02 <b>Note: 1.</b> If JRF candidate is not found then Project Associate-I (PAT – I) / Project Associate-II (PAT – II) may be hired.	<b>JRF:</b> Graduate degree/ B.E./B.Tech/M.Sc (Physics / Engineering Physics / Nano- Technology /Electronics/Electronics & Communication /Electrical/ Mechatronics/ Instrumentation & Control) /ME/M.Tech in Electronics/ Electrical Engineering / Electrical & Electronics Engineering/ Electronics & Communication Engineering/ Electronics & Instrumentation or equivalent; selected through a process described through any one of the following: <b>(a).</b> Scholars who are selected through National Eligibility Tests- CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. <b>(b)</b> The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc. <b>OR</b> <b>PAT – I:</b> B.E./B.Tech/M.Sc (Physics / Engineering Physics / Nano- Technology /Electronics/Electronics & Communication /Electrical/ Mechatronics/ Instrumentation & Control) or equivalent. <b>OR</b> <b>PAT – II:</b> B.E./B.Tech/M.Sc (Physics / Engineering Physics / Nano- Technology /Electronics/Electronics & Communication /Electrical/ Mechatronics/ Instrumentation & Control) or equivalent <b>AND 2</b> years' experience in Research and development in industrial and academic institutions or science and technology organizations and scientific activities and services. <b>OR</b>	35 Years	JRF: Rs. 37,000/- per month PAT – I: Rs. 25,000/- per month and PAT – II: Rs. 28,000/- per month

				Masters/ Integrated Masters in Electronics/ Electrical Engineering / Electrical & Electronics Engineering/ Electronics & Communication Engineering/ Electronics & Instrumentation Engineering.		
3298	GAP-3250 & Development of wide-bandgap semiconductors based highly efficient power electronics systems for Electric Vehicle WP-1.3: Design and Development of Silicon Carbide High Power Devices for Electric Vehicles	Semiconductor Sensors and Microsystems Group (SSMG)	JRF & 02 <b>Note: 1.</b> If JRF candidate is not found then Project Associate-I (PAT – I) / Project Associate-II (PAT – II) may be hired.	<p><b>JRF:</b> Graduate degree/ B.E./B.Tech/M.Sc (Physics / Engineering Physics / Nano-Technology /Electronics/Electronics &amp; Communication /Electrical/ Mechatronics/ Instrumentation &amp; Control) /ME/M.Tech in Electronics/ Electrical Engineering / Electrical &amp; Electronics Engineering/ Electronics &amp; Communication Engineering/ Electronics &amp; Instrumentation or equivalent; selected through a process described through any one of the following:</p> <p><b>(a).</b> Scholars who are selected through National Eligibility Tests- CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.</p> <p><b>(b)</b> The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc.</p> <p><b>OR</b> <b>PAT – I:</b> B.E./B.Tech/M.Sc (Physics / Engineering Physics / Nano-Technology /Electronics/Electronics &amp; Communication /Electrical/ Mechatronics/ Instrumentation &amp; Control) or equivalent.</p> <p><b>OR</b> <b>PAT – II:</b> B.E./B.Tech/M.Sc (Physics / Engineering Physics / Nano-Technology /Electronics/Electronics &amp; Communication /Electrical/ Mechatronics/ Instrumentation &amp; Control) or equivalent <b>AND 2</b> years' experience in Research and development in industrial and academic institutions or science and technology organizations and scientific activities and services.</p> <p><b>OR</b> Masters/ Integrated Masters in Electronics/ Electrical Engineering</p>	35 Years	JRF: Rs. 37,000/- per month PAT – I: Rs. 25,000/- per month and PAT – II: Rs. 28,000/- per month

				/ Electrical & Electronics Engineering/ Electronics & Communication Engineering/ Electronics & Instrumentation Engineering.		
3299	GAP-3250 & Development of wide-bandgap semiconductors based highly efficient power electronics systems for Electric Vehicle WP-1.4: Design and Development of Gallium Oxide Power FETs	Semiconductor Sensors and Microsystems Group (SSMG)	JRF & 01 #If JRF candidate is not found then Project Associate-I (PAT – I) / Project Associate-II (PAT – II) may be hired.	<p><b>JRF:</b> Graduate degree/BE/B.Tech/MSc/Post graduate degree /ME/M.Tech in (Physics / Engineering Physics / Nano-Technology/Electronics/ Electrical Engineering / Electrical &amp; Electronics Engineering/ Electronics &amp; Communication Engineering/ Electronics &amp; Instrumentation//Electrical/ Mechatronics/ Instrumentation &amp; Control) or equivalent; selected through a process described through any one of the following:</p> <p><b>(a)</b> Scholars who are selected through National Eligibility Tests- CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.</p> <p><b>(b)</b> The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc.</p> <p><b>OR</b></p> <p><b>PAT – I:</b></p> <p>B.E/B.tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics &amp; Communication Engineering / Electronics and Instrumentation or equivalent.</p> <p><b>OR</b></p> <p><b>PAT – II:</b></p> <p>B.E/B.tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics &amp; Communication Engineering / Electronics and Instrumentation or equivalent <b>AND</b> 2 years' experience in Research and development in industrial and academic institutions or science and technology organizations and scientific activities and services.</p> <p><b>OR</b></p> <p>Masters/ Integrated Masters in Electronics/ Electrical Engineering / Electrical &amp; Electronics Engineering/ Electronics &amp; Communication Engineering/</p>	35 Years	JRF: Rs. 37,000/- per month PAT – I: Rs. 25,000/- per month and PAT – II: Rs. 28,000/- per month

				Electronics & Instrumentation Engineering.		
3300	GAP-3250 & Development of wide-bandgap semiconductors based highly efficient power electronics systems for Electric Vehicle <b>WP-2.3:</b> Integrated Safety Control Framework for High Voltage DC-DC Converters: Leveraging Monitoring, Predictive Maintenance, and Compliance with Industry Standards	Societal Electronics Group (SEG)	02 JRF & 02 #If JRF candidate is not found, then a Project Associate-I (PAT-I) / Project Associate-II (PAT-II) may be hired.	<p><b>JRF:</b> Graduate degree/BE/B.Tech/Post graduate degree /ME/M.Tech in Electronics/ Electrical Engineering / Electrical &amp; Electronics Engineering/ Electronics &amp; Communication Engineering/ Electronics &amp; Instrumentation/Computer Science or equivalent; selected through a process described through any one of the following:</p> <p><b>(a).</b> Scholars who are selected through National Eligibility Tests- CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.</p> <p><b>(b)</b> The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc.</p> <p><b>OR</b></p> <p><b>PAT – I:</b></p> <p>B.E/B.Tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics &amp; Communication Engineering / Electronics and Instrumentation/Computer Science or equivalent.</p> <p><b>OR</b></p> <p><b>PAT – II:</b></p> <p>B.E/B.Tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics &amp; Communication Engineering / Electronics and Instrumentation or equivalent/Computer Science <b>AND</b> 2 years' experience in Research and development in industrial and academic institutions or science and technology organizations and scientific activities and services.</p> <p><b>OR</b></p> <p>Master's/ Integrated Master's in Electronics/ Electrical Engineering / Electrical &amp; Electronics Engineering/ Electronics &amp; Communication Engineering/ Electronics &amp; Instrumentation Engineering/Computer Science.</p>	35 Years	JRF: Rs. 37,000/- per month PAT – I: Rs. 25,000/- per month and PAT – II: Rs. 28,000/- per month
3301	GAP3250 &	High-Power Microwave	JRF & 01	<b>SRF:</b> JRF with two years of research experience.	35 Years	SRF: 42,000/- per month and

	Development of wide-bandgap semiconductors based highly efficient power electronics systems for Electric Vehicle <b>WP-4.3:</b> Development of Modular Architecture of DC-DC Converters and Onboard Chargers with Condition Monitoring and Health Management	Systems Group (HPMSG)	SRF & 01 <b>Note:</b> 1. If SRF candidate is not found then JRF/Project Associate-I (PAT-I)/ Project Associate-II (PAT-II) may be hired. 2. If JRF candidate is not found then Project Associate-I (PAT-I)/ Project Associate-II (PAT-II) may be hired.	<b>JRF:</b> Graduate degree/BE/B.Tech/Post graduate degree /ME/M.Tech in Electronics/ Electrical Engineering / Electrical & Electronics Engineering/ Electronics & Communication Engineering/ Electronics & Instrumentation or equivalent; selected through a process described through any one of the following: <b>(a).</b> Scholars who are selected through National Eligibility Tests- CSIR-UGC NET including lectureship (Assistant Professorship) and GATE. <b>(b)</b> The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc. <b>OR</b> <b>PAT – I:</b> B.E/B.tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics & Communication Engineering / Electronics and Instrumentation or equivalent. <b>OR</b> <b>PAT – II:</b> B.E/B.tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics & Communication Engineering / Electronics and Instrumentation or equivalent <b>AND</b> 2 years experience in Research and development in industrial and academic institutions or science and technology organizations and scientific activities and services. <b>OR</b> Masters/ Integrated Masters in Electronics/ Electrical Engineering / Electrical & Electronics Engineering/ Electronics & Communication Engineering/ Electronics & Instrumentation Engineering.		JRF: Rs. 37,000/- per month PAT – I: Rs. 25,000/- per month and PAT – II: Rs. 28,000/- per month
3302	GAP3250 & Development of wide-bandgap semiconductors based highly efficient power electronics systems for Electric Vehicle	High-Power Microwave Systems Group (HPMSG)	JRF & 02 If JRF candidate is not found then Project Associate-I	<b>JRF:</b> Graduate degree/BE/B.Tech/Post graduate degree /ME/M.Tech in Electronics/ Electrical Engineering / Electrical & Electronics Engineering/ Electronics & Communication Engineering/ Electronics & Instrumentation or equivalent;	35 Years	JRF: Rs. 37,000/- per month PAT – I: Rs. 25,000/- per month and PAT – II: Rs. 28,000/-

	<b>WP: 2.1</b> Development of wideband-gap based EV inverters, motor drive (400 V-1500 V) and Integrated Safety Control Framework		(PAT – I) / Project Associate-II (PAT – II) may be hired.	<p>selected through a process described through any one of the following:</p> <p><b>(a).</b> Scholars who are selected through National Eligibility Tests- CSIR-UGC NET including lectureship (Assistant Professorship) and GATE.</p> <p><b>(b)</b> The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc.</p> <p><b>OR</b></p> <p><b>PAT – I:</b></p> <p>B.E/B.tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics &amp; Communication Engineering / Electronics and Instrumentation or equivalent.</p> <p><b>OR</b></p> <p><b>PAT – II:</b></p> <p>B.E/B.tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics &amp; Communication Engineering / Electronics and Instrumentation or equivalent <b>AND</b> 2 years experience in Research and development in industrial and academic institutions or science and technology organizations and scientific activities and services.</p> <p><b>OR</b></p> <p>Masters/ Integrated Masters in Electronics/ Electrical Engineering / Electrical &amp; Electronics Engineering/ Electronics &amp; Communication Engineering/ Electronics &amp; Instrumentation Engineering.</p>		per month
3303	<p>GAP3250 &amp; Development of wide-bandgap semiconductors based highly efficient power electronics systems for Electric Vehicle</p> <p><b>WP-4.2:</b> Development of Modular Converter Architecture of High Frequency Inverter</p>	High-Power Microwave Systems Group (HPMSG)	<p>JRF &amp; 01 and SRF &amp; 01</p> <p>Note:1. If SRF candidate is not found then JRF/Project Associate-I(PAT-I)/Project</p>	<p><b>SRF:</b> JRF with two years of research experience.</p> <p><b>JRF:</b> Graduate degree/BE/B.Tech/Post graduate degree /ME/M.Tech in Electronics/ Electrical Engineering / Electrical &amp; Electronics Engineering/ Electronics &amp; Communication Engineering/ Electronics &amp; Instrumentation or equivalent; selected through a process described through any one of the following:</p> <p><b>(a).</b> Scholars who are selected through National Eligibility Tests- CSIR-UGC NET including</p>	35 Years	<p>SRF: 42,000/- per month and JRF: Rs. 37,000/- per month</p> <p>PAT – I: Rs. 25,000/- per month and PAT – II: Rs. 28,000/- per month</p>

			Associate-II(PAT-II) may be hired. 2. If JRF candidate is not found then Project Associate-I(PAT-I)/ Project Associate-II(PAT-II) may be hired.	lectureship (Assistant Professorship) and GATE. <b>(b)</b> The selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MoE, ICAR, ICMR, IIT, IISc, IISER, NISER etc. <b>OR</b> <b>PAT – I:</b> B.E/B.tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics & Communication Engineering / Electronics and Instrumentation or equivalent. <b>OR</b> <b>PAT – II:</b> B.E/B.tech in Electrical Engineering / Electronic Engineering / Electrical and Electronics Engineering / Electronics & Communication Engineering / Electronics and Instrumentation or equivalent <b>AND</b> 2 years experience in Research and development in industrial and academic institutions or science and technology organizations and scientific activities and services. <b>OR</b> Masters/ Integrated Masters in Electronics/ Electrical Engineering / Electrical & Electronics Engineering/ Electronics & Communication Engineering/ Electronics & Instrumentation Engineering.		
3304	MMP015201 & W.P-2.2 Design and Development of Un-cooled Compact Pyroelectric-based THz Detector Developing and Leveraging Terahertz Technology Advancements in CSIR(DELTA) Mission	Semiconductor Process Technology Group	Project Assistant & 01	B.Sc. / 3 Years Diploma in Electronics or equivalent Desirable Qualification : Knowledge of semiconductor processes and device fabrications.	28 Years	Rs. 20000/-

\*Candidates with above qualification without experience can also apply they shall be eligible for the position of PAT-I

# For PAT – I:

(i) ₹31,000/- + HRA to Scholars who are selected through-

- (a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or
- (b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.



(ii) ₹25,000/- + HRA for others who do not fall under (i) above.

\* For PAT – II:

(i) ₹35,000/- + HRA to Scholars who are selected through-

(a) National Eligibility Test- CSIR-UGC NET including lectureship (Assistant Professorship) or GATE or

(b) A selection process through National level examinations conducted by Central Government Departments and their Agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISC and IISER etc.

(ii) ₹28,000/- + HRA for others who do not fall under (i) above

**For AcSIR Ph.D. (Engineering/IDDP) Students**

- 1. Those who does not have any fellowship can apply only when their research domain of Ph.D. is aligned with the research area of that project. Prior to applying, the Ph.D. student needs to have the permission of the Supervisor(s), PI of the project and approval from the Director. Scanned copy of the same should be uploaded on the portal.***
- 2. Those who are having fellowship, they are not eligible to apply.***

Interested candidates fulfilling the above qualifications must apply through online portal available on CSIR-CEERI website. A print out of the Application Form must be taken by the candidate after successfully submission of the form. The shortlisted candidates will be informed through 'E-mail/Institute website' along with the date and time for interview.

Candidates are requested to frequently browse the Institute website. Shortlisted candidates will have to appear for the interviews through "Microsoft Teams" (MS Teams). Candidates will be allowed as a Guest in MS Teams, for which the link will be send to your registered Email.

The selected candidates are expected to join immediately upon selection and bring with them all ORIGINAL TESTIMONIALS including application form, mark sheets & certificates and also a passport size photograph at the time of joining, failing which their engagement can be withdrawn.

<b>For Recruitment queries:</b> <b>Sh. Rajan Tirkey</b> <b>Section Officer</b> <b>CSIR-CEERI, Pilani</b> <b>Email: <a href="mailto:recruitment@ceeri.res.in">recruitment@ceeri.res.in</a></b> <b>Phone:01596-252275</b>	<b>For Technical queries:</b> <b>Sh. Sanjeev Kumar</b> <b>Advanced Information Technologies Group</b> <b>CSIR-CEERI, Pilani</b> <b>Email: <a href="mailto:sanjeevkumar@ceeri.res.in">sanjeevkumar@ceeri.res.in</a></b> <b>Phone:01596-252282</b>
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**Other Conditions:**

- Candidates who have already served CSIR-CEERI or any other lab/ Institute of CSIR as Project Staff such as Project Assistant/ PAT-I/II/ JRF (in contract R&D Projects)/ SRF (in contract R&D Projects)/ Research Associate/ PAT-I/II etc. for a total period of 6 years or more are not eligible for these engagements. The candidates who have served for a period less than 6 years will have tenure up to remaining period till completion of Six years.
- Tenure of Engagement:** Six months initially or co-terminus with the present project or till such time the job to be performed by Project Staff in the project exists, whichever is earlier. The tenure may be extended in steps of six months for the duration of Project based on satisfactory performance. The total tenure as Research Associate, Project Assistant, PAT-I/II, JRF/SRF etc. shall not exceed 6 years in any case. The total tenure of Six years shall be calculated as per periods spent on one Project and/ or different Projects taken together in CSIR-CEERI, Pilani and/or any other Lab./Instt. of CSIR as RA, Project Assistant, PAT-I/II etc. or as any other designation of equal

status.

**3. All applicants must fulfil the essential qualification and other conditions stipulated in the advertisement as on the last date of receipt of the online applications. They are advised to satisfy themselves before applying that they possess at least the essential qualifications laid down as on the last date of receipt of online application.**

4. The date for determining the upper age limit, essential educational qualification and/or experience shall be the last date of submission of online application.
5. The engagement of Scientific Administrative Assistant/Project Assistant, PAT-I/II, JRF, SRF etc. will be made on behalf of the Sponsor of the Projects. These are not CSIR-CEERI/CSIR posts and will not confer any right on the incumbent to claim, implicit or explicit, on any post in CSIR-CEERI/CSIR.
6. Reservation: As regards reservation, if all things are equal, SC/ST/OBC/PWBD/EWS candidates may be given preference over General candidates so as to ensure their representation.
7. **Candidates shall upload scanned copies of following documents on Project Staff Recruitment Portal of CSIR-CEERI:**
  - a. Candidates shall upload scanned copies of following academic certificates while applying for project staff positions :-
    - i. 10<sup>th</sup> Standard
    - ii. 12<sup>th</sup> Standard
    - iii. Diploma Degree
    - iv. Graduation Degree (B.A./ B.Com./ BBA/ BCA/ B.Sc./ B.E./ B.Tech. etc.)
    - v. Post-Graduation (M.Sc./ M.E./ M.Tech./ MS etc.)

Educational Qualification Certificate as mentioned in the advertisement essential qualification/ Semester Mark sheet or Final Mark sheet is compulsory. Along with this, Degree Award Certificate or Provisional Award Certificate. Percentage or CGPA/ DGPA should be clearly displayed on any one of the certificates. If certificate doesn't have proper percentage/ CGPA/ DGPA/ Grade then the application may not be considered. The candidate must ensure to provide proper certificates to ensure their eligibility for the positions they are applying for.
  - b. Date of Birth Proof (Any valid certificate having DOB such as 10<sup>th</sup> Standard, DOB Certificate, etc.)
  - c. Category certificate (in case of SC/ ST/ OBC/ PWBD/ EWS candidates)
  - d. NET/ GATE/ Any other equivalent exam qualification certificate (If applicable)
  - e. Experience certificate (Mandatory for the posts where experience is essential qualification)
  - f. Other certificates (If applicable)
8. If it is found at any stage of the recruitment process or thereafter that the candidates do not fulfil the eligibility criteria, their candidature shall be cancelled without assigning any reason. Further, the decision of CSIR-CEERI in regard to the selection process will be final and binding to all concerned. No correspondence in this regard will be entertained by the Institute.
9. Received applications will be screened by duly constituted Standing Screening Committee. The Screening Committee may fix different set of criteria to short-list the candidates as per the project requirement. Screening Committee will scrutinize and shortlist all the applications received for the specified project. Shortlisted candidates will be called for online interview. In case of false information received through online application, the competent authority will cancel the candidature of the candidate and debar for attending the interview in future.
10. Shortlisted candidates may be called for interview through E-mail intimating the **date & time, link/ reference of online mode, terms & conditions**, if any. The candidate will be allowed to join only after verification of original certificates/ documents and other information found correct.

11. Based on the performance of the candidates and availability of suitable candidates, Selection Committee may also recommend Panels for future engagement in different projects as and when need arises. The engagement process will be complete after the joining of the candidates.
12. CSIR-CEERI reserves the right to cancel or withdraw the Offer of Engagement in case of any discrepancy found, in the candidature of any empaneled candidate at any stage.
13. The number of positions may be increased or decreased as per the requirements.
14. Other Terms & Conditions will be governed as per guidelines issued by the funding agency/CSIR/CEERI for the engagement of above Project Staff as amended from time to time.

**The last date & time for applying applications is 31.07.2025 till 05:00 P.M. Application received after last date will not be entertained.**

**SECTION OFFICER**

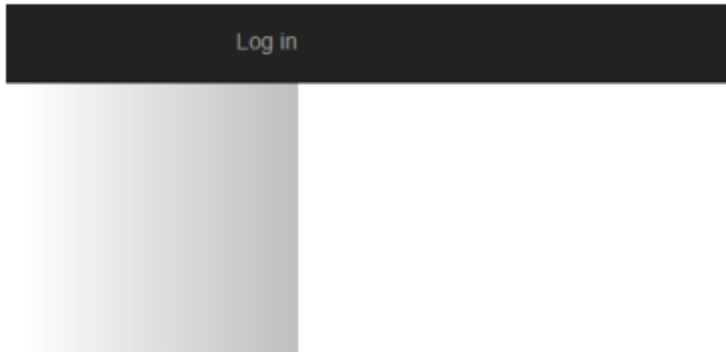
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## HOW TO APPLY

### STEP -1

Go to the link <https://psrecruit.ceeri.res.in/>

Click on button **login** present on right top corner



After this click on **Register** button present on top side of right-hand side.

**Register** Create a new account

First name

Last name

Email

Password

Confirm password

**Register**

Fill all details given and click on **Register**. Don't forget your password. Wait for few second

Do not click on anywhere or any other button. Page do nothing but it will register you on our portal. But your **account is still not active**. You are not able to login.

### STEP -2

Then send your email and name to [sanjeevkumar@ceeri.res.in](mailto:sanjeevkumar@ceeri.res.in) (between Monday to Friday, from 9:30 AM to 6:00 PM)

Technical person will activate the your account and reply back to that email.

### **STEP -3**

After Activation of your account you can **login** using your **email id** and **Password**.

Now you can apply for the posts given in advertisement.

### **END**

### **IMPORTANT NOTE**

One thing to remember that **don't forget your password**. You have to remember your password (Note down in phone or somewhere else). If you forget your password then you have to Register again i.e. follow all the steps given from starting in this document. **There is no option of reset password.**